Journal of Scientific and Engineering Research, 2018, 5(10):140-142



Review Article

ISSN: 2394-2630 CODEN(USA): JSERBR

Reversible Computing

M. N. O. Sadiku¹, S. R. Nelatury², S.M. Musa¹

¹College of Engineering, Prairie View A&M University, Prairie View, TX 77446 ²School of Engineering and Engineering Technology, Pennsylvania State University, Erie, PA 16563-1701

Abstract Reversibility is one of the basic microscopic physical laws of nature. Reversible computing refers to the computation that could always be reversed to recover its earlier state. It is based on reversible physics, which implies that we can never truly erase information in a computer. Reversible computing is very difficult and its engineering hurdles are enormous. This paper provides a brief introduction to reversible computing.

Keywords reversible computing, reverse computation, quantum computing, adiabatic computing

Introduction

The world of computing is in transition. Conventional computing models such as Turing machines and random access machines destroy information at each computation step. As chips become smaller and faster, they dissipate or waste energy. Reverse computing has been found to provide more efficient forms of computation. Reverse computing means computation that takes place in steps that are time reversible. If a reversible computation yields a series of outputs, then the inverse computation on these outputs should reproduce the original states [1].

This idea of reversible computing goes to the very heart of thermodynamics and information theory. It is the only possible way to keep improving the cost and energy efficiency of computers far into the future. The idea is to get rid of the immense energy dissipation and heat generation caused by the irreversibility of conventional computing processes. Reversible computing needs to be adiabatic, as nearly as possible. That essentially means that there is no heat flow between subsystems and the system is thermodynamically reversible.

Reversible Computing Systems

The idea of reversible computing came from physicist Rolf Landauer of IBM, who published a paper in 1961 titled "Irreversibility and Heat Generation in the Computing Process." According to Landauer, in order for a computational process to be physically reversible, it must also be logically reversible. This means that the most fundamental laws of physics are reversible. Landauer's idea has since been experimentally confirmed. The reversibility of physics implies that we can never truly erase information in a computer. Reversible computing did not receive much attention in the past because it is hard to implement.

Reversible computing obeys the von Neumann-Landauer principle which implies that ordinary irreversible logic operations incur a fundamental minimum energy cost. It difficult for practical technology to progress far beyond current levels of energy efficiency if reversible computing principles are not applied. Implementing reversible systems requires estimating its cost and judging its limits. In these systems computation is often carried out in a different manner from conventional (or irreversible) computing systems [2].

Reversible computing operations can be carried out by idealized lossless electronic circuits that used inductors and capacitors, with no resistors damping the flow of energy. Unfortunately, these idealized electronic implementations cannot be built in practice [3].



Applications

There are some applications in which reverse computation is natural. Examples include quantum computer simulation, queuing network simulation, and the low-power CMOS (complementary metal-oxide-semiconductor). Quantum computing is closely related to reverse computing and has a lot in common with reversible computing [4]. Molecular quantum-dot cellular automata (QCA) is a practical implementation of reversible computing. It is a promising nanoechnology that offers significant improvements such as power dissipation over CMOS. It is possible to build reversible logic circuits using QCA. Reverse computation is well suited for the optimistic simulation of queuing network models [5-6].

Typical models of reversible computing include reversible Turing machines, reversible cellular automata, reversible logic elements, and reversible physical models [7]. A reversible computer promises to reduce energy consumption and thereby becomes very useful in laptops, mobile devices, and other battery operated devices which run on limited-life batteries. It would require new hardware designs, software, development tools, programming languages, and compilers [8]. Reversible machines will require special reversible programs and several reversible programming languages have been proposed. Calculations can be performed by reversible machines made of only reversible primitives.

Benefits and Challenges

Reversible computing could offer dramatic improvements in energy efficiency. There is no limit to the efficiency of reversible computing. In other words, there is no limit on the amount of reversible computation that can be performed using a fixed amount of energy. For this reason, some researchers are looking to reversible computing to save energy.

Perhaps the main benefit for building technologies that implement reversible computing is that they offer what is predicted to be the only potential way to improve the computational energy efficiency of computers beyond the fundamental von Neumann-Landauer bound [9].

Although our understanding of how to design machines based on reversible logic has improved dramatically, some significant research challenges remain. No one has successfully built a properly reversible logic gate. Reversible computing has experienced slow progress. It may require a concerted effort on the part of the semiconductor industry, the computing industry, and government to make significant research progress in reversible computing.

Conclusion

Reversible computing is emerging as one of the most exciting dimensions in computing for the future. It aims at realizing reversible versions of conventional computations in order to reduce power consumption. Although the idea is still ahead of its time, some practical reversible computing technology might not be far away. Research on developing fast reversible circuits is still ongoing. For more information about reversible computing, one should consult books [10-11].

References

- [1]. "The fantastical promise of reversible computing," January 25, 2011 https://www.technologyreview.com/s/422511/the-fantastical-promise-of-reversible-computing/
- [2]. K. Morita, "Reversible computing systems, logic circuits, and cellular automata," *Proceedings of the Third International Conference on Networking and Computing*, 2012.
- [3]. M. P. Frank, "The future of computing depends on making it reversible," August 2017, https://dewaynenet.wordpress.com/2017/09/10/the-future-of-computing-depends-on-making-it-reversible/
- [4]. M. N. O. Sadiku, M. Tembely, and S.M. Musa, "Quantum computing: A primer," International Journal of Advanced Research in Computer Science and Software Engineering, vol. 7, no. 11, Nov. 2017, pp. 129-130.



- [5]. C. D. Carothers, K. S. Perumalla, and R. M. Fujimoto, "Efficient optimistic parallel simulations using reverse computation," ACM Transactions on Modeling and Computer Simulation, vol. 9, no.3, July 1999, pp. 224–253.
- [6]. J. Huang, X. Ma, and F. Lombardi, "Energy analysis of QCA circuits for reversible computing," *Proceedings of the Sixth IEEE Conference on Nanotechnology*, 2006.
- [7]. K. Morita, "Reversible computing and cellular automata—A survey," *Theoretical Computer Science*, vol. 395, 2008, pp. 101–131.
- [8]. L. D. Paulson, "Reversible computing may improve mobile performance," *Computer*, March 2004, p. 21.
- [9]. "Reversible computing," *Wikipedia*, the free encyclopedia https://en.wikipedia.org/wiki/ Reversible_computing
- [10]. A. D. Vos, *Reversible Computing: Fundamentals, Quantum Computing, and Applications.* John Wiley, 2010.
- [11]. K. S. Perumalla, Introduction to Reversible Computing. Boca Raton, FL: CRC Press, 2014.

About the Authors

Matthew N.O. Sadiku (sadiku@iee.org) is a professor at Prairie View A&M University, Texas. He is the author of several books and papers. He is an IEEE fellow. His research interests include computational electromagnetics and computer networks.

Sudarshan R. Nelatury (srn3@psu.edu) is an associate professor at Penn State University, The Behrend College, Erie, Pennsylvania. His teaching and research interests lie in electromagnetics and signal processing.

Sarhan M. Musa (smmusa@pvamu.edu) is a professor in the Department of Engineering Technology at Prairie View A&M University, Texas. He has been the director of Prairie View Networking Academy, Texas, since 2004. He is an LTD Sprint and Boeing Welliver Fellow.

